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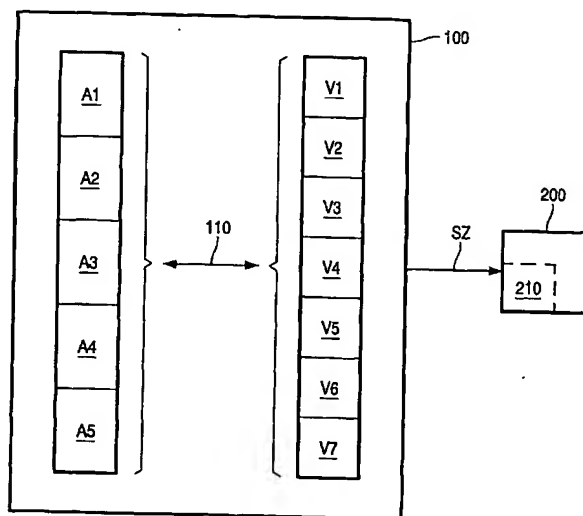
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(54) Title: **CIRCUIT ARRANGEMENT AND METHOD FOR PROTECTING ELECTRONIC COMPONENTS AGAINST IL-
LICIT MANIPULATION**

(57) **Abstract:** To provide a microelectronic circuit arrangement (100) and a method for protecting at least one electronic component against illicit manipulation and/or unauthorized access, in which circuit arrangement (100) and method there are no opportunities for manipulation even during or in connection with the start-up procedure, it is proposed that there be arranged at least activating unit (A_i ; $i = 1, 2, 3, 4, 5$) for checking whether at least one activating condition is met and for activating at least one preventing unit (V_j ; $j = 1, 2, 3, 4, 5, 6, 7$) that is also associated with the circuit arrangement (100) and that is connected to the activating unit (A_i), by means of which preventing unit (V_j) the component (200) can be at least partly de-activated and/or at least partly destroyed in the event of illicit manipulation and/or unauthorized access.